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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10054373	FILING DATE 01/22/2002	CLASS 361 444	SUBCLASS 224	GAU 3852	EXAMINER F. J. ...
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**\*\*APPLICANTS:** Chen Jiong; Chen Jihliang; Qiao Jianmin;  
263

**\*\*CONTINUING DATA VERIFIED:** OK

**\*\* FOREIGN APPLICATIONS VERIFIED:** OK

PG-PUB	DO NOT PUBLISH <input checked="" type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		AIBT 12K09
Verified and Acknowledged Examiners's initials ASH		
TITLE : Semiconductor processing chamber substrate holder method and structure		

U.S. DEPT. OF COM. / PAT. & TM. - PTO-4261 (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>Assistant Examiner</b>	<b>CLAIMS ALLOWED</b>		
			Total Claims	Print Claim for O.G.	
<b>ISSUE FEE</b>		<b>Primary Examiner</b>	<b>DRAWING</b>		
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.	Print Fig.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	<b>Application Examiner</b>		
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